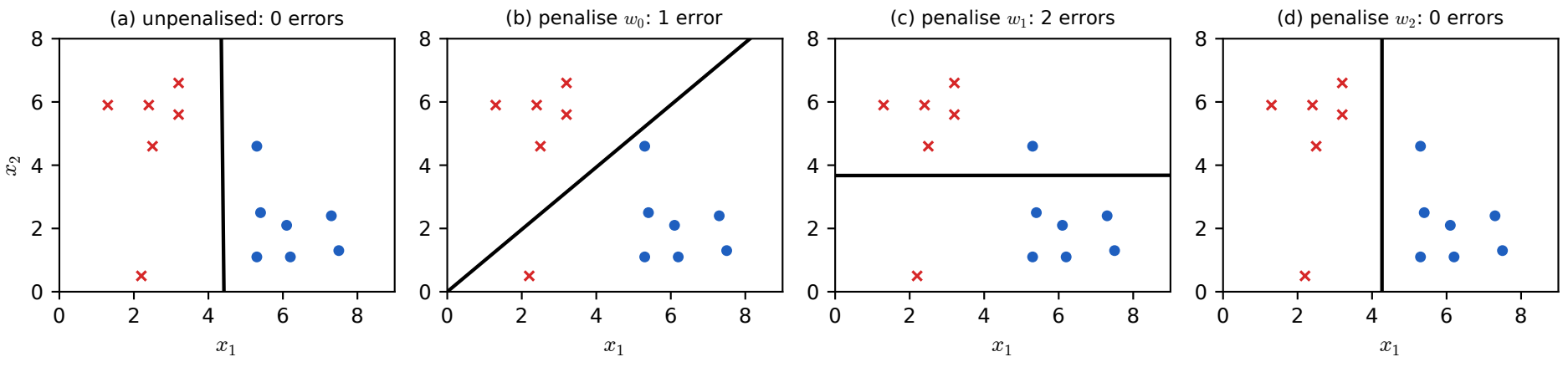

(a) unpenalised: 0 errors
(b) penalise w_0: 1 error
(c) penalise w_1: 2 errors
(d) penalise w_2: 0 errors
x_1
x_2
0
2
4
6
8